

Electronic Acknowledgement Receipt

EFS ID:	7993246
Application Number:	10801503
International Application Number:	
Confirmation Number:	1822
Title of Invention:	Fractional-type Phase-Locked Loop circuit with compensation of phase errors
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	2110-111-03_RCE_20100712.pdf	38433 2d8591b7c1a16c792bc95a1c2ee3d8c504dc4995a	no	3

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2	Fee Worksheet (PTO-875)	fee-info.pdf	30176 1a83ee48174ca88d4c74b4c710f17b183f591c9	no	2
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Warnings:

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